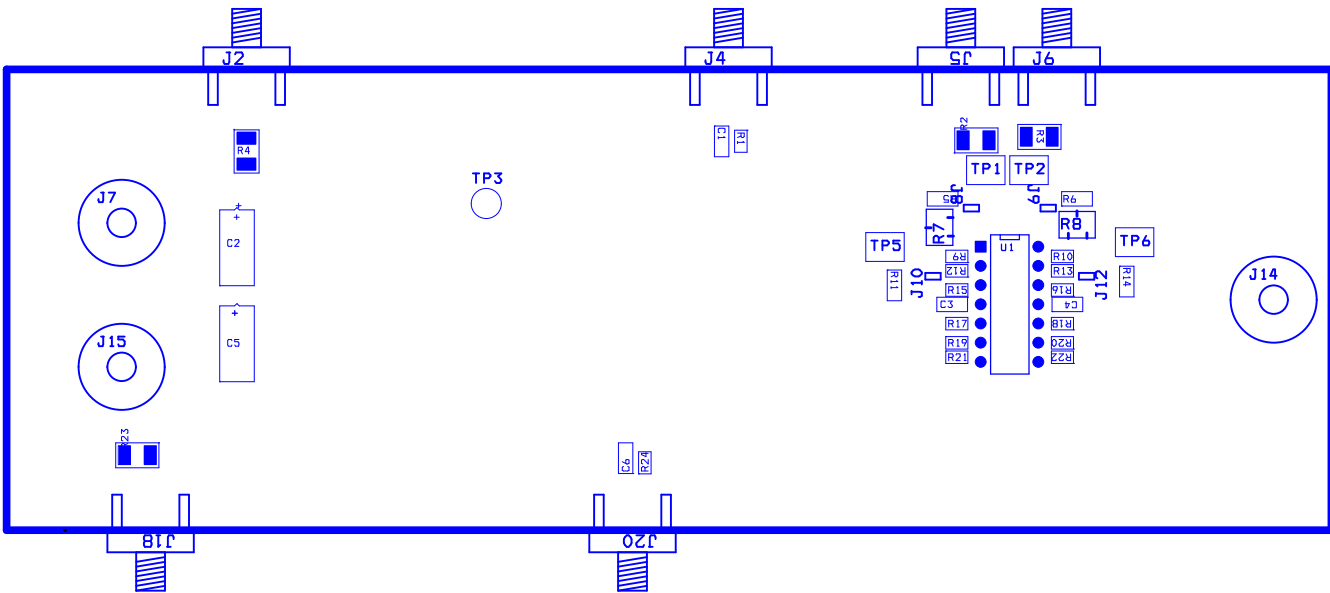


DESCRIPTION

ZONE	REV	REVISIONS	DATE	APPROVED
-	A	Release	19oct2002	Rako



UNLESS OTHERWISE NOTED ALL DIMENSIONS ARE IN MILS

Material: See Notes	DRAWN: Paul Rako	DATE: 19oct2002	National Semiconductor Amplifier Applications
Finish: See Notes	CHECKED: Paul Rako	DATE: 19oct2002	
Tolerances		ENGINEER: Paul Rako	DATE: 19oct2002
1PL	2PL	3PL	Assembly Drawing A DRAWING: Quad DIP CMRR Board A SCALE: 1:1 Sheet 1 of 1 REV
.02	.01	.005	
DO NOT COPY DISPLAY OR USE DRAWING WITHOUT AUTHORIZATION DO NOT SCALE DRAWING			